4D RTK: clock solutions and performance

A/Prof Yanming Feng* Dr Bofeng Li*,** *Queensland University of Technology **Tongji University, PR China



Overview of GPS Timing applications

Me	ethod	Mode	Accuracy	Application
Sin Re	ngle point/ ceiver/epoch	One-way mode (OWM)	20-30 ns	Time transfer
Dif Sin	ferential GPS ngle epoch	Common-view mode (CVM)	5-10 ns	high precision time frequency transfer
Tw Ion tim	ro receivers, ng observation ne	Melting-pot mode (MPM)	1-3 ns	Remote clock control and steering
Sin Coi	ngle receiver ntinuous data	РРР	0.1 ~0.3ns	Comparison with TWSTFT

3D RTK Vs 4D RTK

3D RTK:

- Clock biases cancelled via doubledifferencing, only 3 D coordinates to be determined in real time
- 4D RTK:
 - Relative clock biases included in the singledifferences, 3D position states and 1D clock bias are determined every epoch (without clock models)
 - Or 1D relative clock is determined if 3D coordinates are given













FIG Congress 2010 Facing the Challenges – Building the Capacity Sydney, Australia, 11-16 April 2010

	Data and process settings	
Source of data	http://www.ngs.noaa.gov/CORS/cors-data.html. RINEX data: Sites: P474-P478 on 1/1/2007	
Observation types	L1, L2, P1,P2	
Distance	21km	
Cutoff elevation	15 degrees	
Number of SVs	8	
Data period	00:09:30~01:36:00	
Sample rate	15 seconds	
Total of epochs	347	
Code noise P1,P2 (SD)	0.363m (estimated)	
hase noise L1.L2 (SD)	0.010cm (estimated)	









Figure 4 (a) Illustrations of the code STD variations and the overall STD of code P1 and P12 are 0.363m and 0.263 m respectively. The accuracy of the RTK clock solutions reaches the level of 3ns within a few minutes, and converges to 1.5 cm .beyond the 150th epochs.

Conclusions (1)

Theoretical results:

- The existing 3D RTK model based on the DD code and phase measurements is a reduced case of the 4D RTK
- The 3D position states are determined with the DD phase measurements as usual using the existing ILS procedure, the relative clock bias and the SD phase ambiguities are estimated simultaneously epoch by epoch with the SD code and phase observations
- The constant nature of the SD phase ambiguities allows their estimations to be improved accumulatively; in turn the relative clock solutions are improved from time to time without clock modeling
- When the user baselines or coordinates are known, the RTK clock solutions can be determined directly from the SD phase and code measurements very easily.

Conclusions (2)

Experimental results:

- Within the first few minutes of observations, the SD phase biases fall into the range of 0.3 cycles, achieving a 0.1 to 0.2 ns clock uncertainty.
- Beyond 150 epochs, the SD phase biases are stabilized within the range of +/-1.5 cm, thus leading the clock biases estimation to the precision and accuracy of 0.05~0.1 ns.
- The numerical results are consistent with theoretical performance prediction
- The effects of inter-frequency biases on SD must be removed



- The 4D RTK method can potentially provide the most accurate time synchronisations: 0.05-0.1 ns
- The precisely synchronised CORS stations can provide not only provide 3D coordinate datum, but also time datum on a secondly basis
- PPP clocks solutions require satellite clock solutions, which are more complicated
- Comparison with PPP clock solutions is under way

Thank you!

y.feng@qut.edu.au

FIG Congress 2010 Facing the Challenges – Building the Capacity Sydney, Australia, 11-16 April 2010